

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S77	37	S74 and (metal chip resistor).dm.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 10:46
S76	2	S74 and (metal chip resistor insulation (side or surface)).dm.	US-PGPUB; USPAT; USOCR; EPO; JPO	AND	ON	2009/07/01 10:45
S75	1	S74 and (metal chip resistor insulation (side or surface)).dm.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 10:45
S74	6079	S68 or S69 or S70 or S71 or S72	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 10:17
S73	1510	S71 or S72	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 09:50
S72	603	338/332.cds.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 09:50
S71	951	338/309.cds.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 09:50
S70	2002	438/123.cds.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 09:49

S69	1012	257/e21.518.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 09:49
S68	1609	257/536.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	SAME	ON	2009/07/01 09:49

7/1/2009 12:17:18 PM**C:\Documents and Settings\jingham\My Documents\EAST\Workspaces
\passive\10 533489 chip resistor with insulation on sides and top.wsp**